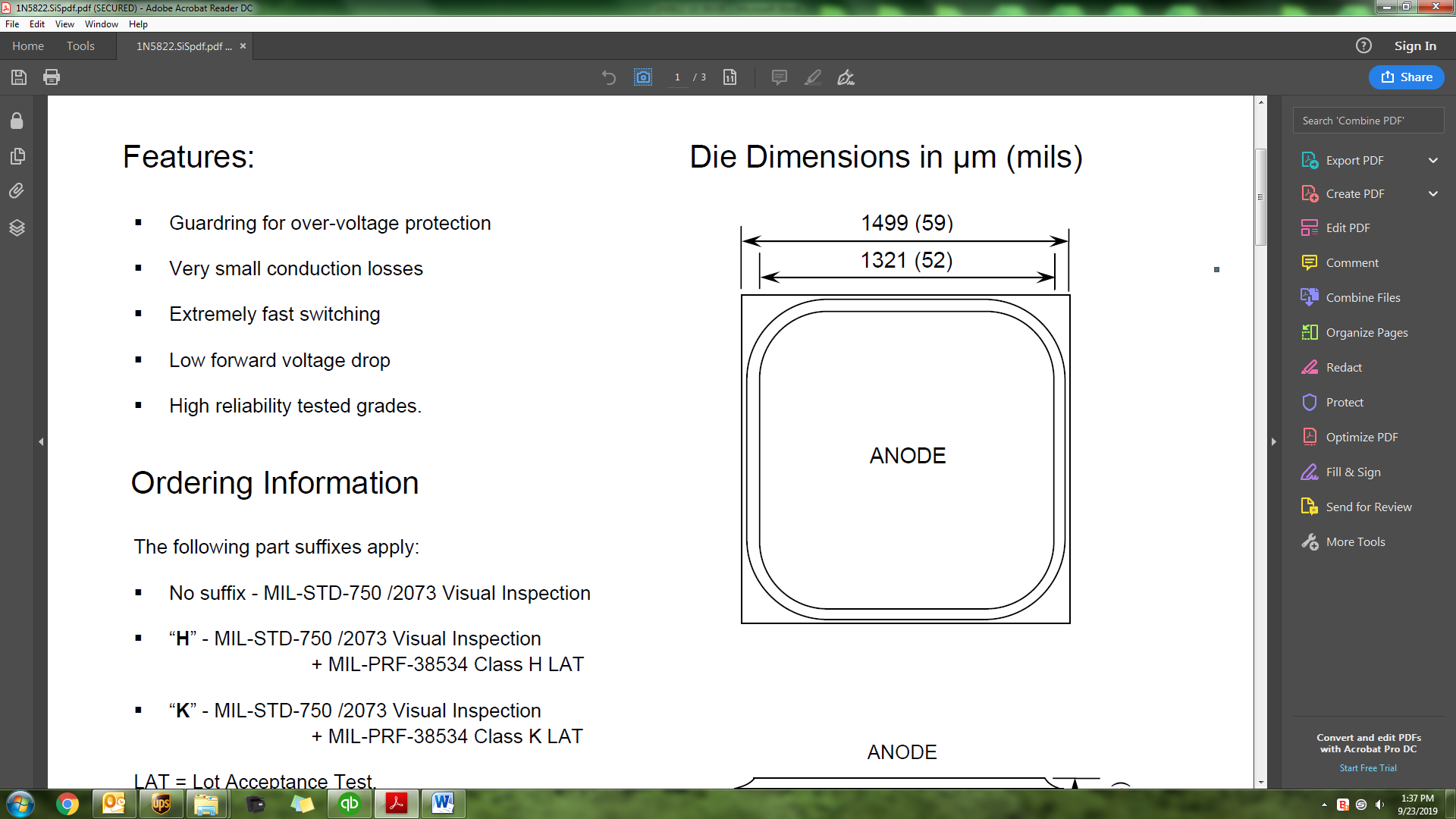
Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**.059”**



**.052 X .052”**

**ANODE**

**.059”**

**Top Material: Al**

**Backside Material: TiNiAg**

**Bond Pad Size: .052” X .052”**

**Backside Potential: Cathode**

**APPROVED BY: DK DIE SIZE .059” X .059” DATE: 11/10/21**

**MFG: SILICON SUPPLIES THICKNESS .011” P/N: 1N5822**

**DG 10.1.2**

#### Rev B, 7/1